

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): HIMI et al.	Atty. Dkt.: 01-100-DIV
Serial No.: Unknown	Group Art Unit:
Filed: Concurrently herewith	Examiner:
Title: METHOD FOR MANUFACTURING SEMICONDUCTOR SUBSTRATE	

Commissioner for Patents  
Arlington, VA 22202

Date: November 20, 2003

**INFORMATION DISCLOSURE STATEMENT**

Sir:

Pursuant to 37 C.F.R. §1.56, the reference(s) listed on the attached Form PTO-1449 is/are being submitted for consideration by the Examiner without any admission that it/they constitute(s) statutory prior art, or without any admission that it/they contain(s) subject matter that anticipates the invention or renders the invention obvious to a person of ordinary skill in the art.

The Examiner is requested to initial the attached PTO Form-1449 and to return a copy of same to the undersigned attorney as proof that the listed reference(s) has/have been considered and made of record.

Respectfully submitted,



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FORM PTO-1449	ATTY. DKT NO.	1-100-DIV	SER. NO.
	APPLICANT	HIMI et al.	
	FILING DATE	Nov. 20, 2003	GROUP

## REFERENCE DESIGNATION

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS
		5,742,093	Apr. 21, 1998	Kunihiro		
		5,141,879	Aug. 25, 1992	Goronkin et al.		
		4,376,138	Mar. 8, 1983	Alfernness et al.		
		4,469,527	Sep. 4, 1984	Sugano et al.		
		5,930,642	Jul. 27, 1999	Moore et al.		
		5,961,713	Oct. 5, 1999	Wijaranakula		

## FOREIGN PATENT DOCUMENTS

## TRANSLATION

		DOCUMENT NUMBER	DATE	COUNTRY	NAME	CLASS	SUB CLASS	YES	NO
		63-29937	2/88	JAPAN				X	
		64-11316	1/89	JAPAN				X	
		1-290229	11/89	JAPAN				X	
		3-181115	8/91	JAPAN				X	
		5-235007	9/93	JAPAN				X	
		6-283693	10/94	JAPAN				X	
		63-29937 X	2/88	JAPAN				X	
		56-164547	12/81	JAPAN				X	

## OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

EXAMINER		DATE CONSIDERED